



ins
A1

IMAGE-CAPTURING DEVICE AND ELECTRONIC CAMERA

INCORPORATION BY REFERENCE

The disclosure of the following priority application
5 is herein incorporated by reference:
Japanese Patent Application No. 11-267166, filed
September 21, 1999

BACKGROUND OF THE INVENTION

10 1. Field of the Invention

The present invention relates to an image-capturing device that utilizes a solid image-capturing element and an electronic camera provided with the image-capturing device.

15 2. Description of the Related Art

Image-capturing devices in the known art include those that employ two-dimensionally arrayed photoelectric conversion elements such as photodiodes to convert light to electrical charges corresponding to the light
20 intensity distribution, transfer the stored charges from the individual photoelectric conversion elements to a floating diffusion amplifier via a charge transfer element such as a CCD (charge-coupled device) and generate an output achieved by converting the stored
25 charges to voltages and amplifying them.

00500500-021500

FIG. 5 illustrates the structure of an image-capturing device in the prior art.

A plurality of photoelectric conversion elements 1 are two-dimensionally arrayed on a semiconductor substrate (silicon substrate) 20. A charge transfer element 2 is provided in correspondence to each column of photoelectric transfer elements 1 to transfer the stored charges in the direction of the column. A charge transfer element 3 that transfers electrical charges in the direction of the row is connected at the ends of the individual column charge transfer elements 2 along the direction of their charge transfer so that the charges that have been transferred in the direction of the column are further transferred in the direction of the rows. A floating diffusion amplifier 4 is connected to the end of the row charge transfer element 3 along the direction of its charge transfer to output voltages achieved by converting the electrical charges to voltages and then amplifying them through an output terminal 8.

In this specification, the floating diffusion amplifier, which is connected at the end of charge transfer elements along the direction of transfer and converts the transferred charges to voltages and then amplifies them, is to be simply referred to as an "output amplifier."

056098-01

5 In order to attain the above object, an image-
capturing device according to the present invention
comprises: a plurality of photoelectric conversion
elements that are two-dimensionally arrayed; a charge
transfer circuit that transfers electrical charges from
10 the photoelectric conversion elements; and an amplifier
that is connected to an end of the charge transfer
circuit along a direction of charge transfer, converts an
electrical charge into voltage, and amplifies the voltage,
and at least the photoelectric conversion elements, the
15 charge transfer circuit and the amplifier are provided on
a single semiconductor substrate. And the image-
capturing device further comprises an amplifier power
control circuit that controls power to the amplifier in
conformance to a control signal provided from outside.

Also, it is preferred that the charge transfer
25 circuit transfers the electrical charges to the amplifier

09500988-021500

in conformance to the control signal when discharging unnecessary electrical charges and reading out electrical charges from the photoelectric conversion elements, and the bias current to the amplifier is reduced in conformance to the control signal at other times. In this case, it is preferred that the control device controls the image-capturing device so that the normal bias current is supplied to the amplifier in conformance to the control signal when exposure is performed over a length of time equal to or less than a specific length of time.

An image-capturing device according to the present invention comprises: a plurality of photoelectric conversion elements; and a heat generating component that constitutes a local heat source. The plurality of photoelectric conversion elements and the heat generating component are provided on a single semiconductor substrate. And the image-capturing device further comprises a heat generating component power control circuit that controls power to the heat generating component in conformance to a control signal provided from outside.

In this image-capturing device, it is preferred that the plurality of photoelectric conversion elements are two-dimensionally arrayed on the semiconductor substrate.

Also, it is preferred that the heat generating component is an A/D converter.

Also, it is preferred that the heat generating component is a signal processor.

5

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 illustrates the structure adopted in an embodiment;

FIG. 2 illustrates the structure adopted in an embodiment;

FIG. 3 presents a time chart of an image-capturing operation performed when a long exposure period is set;

FIG. 4 presents a time chart of an image-capturing operation performed when a short exposure period is set;

FIG. 5 illustrates the structure adopted in an image-capturing device in the prior art;

FIG. 6 illustrates the problems of the image-capturing device in the prior art;

FIG. 7 illustrates dark current shading occurring in the image-capturing device in the prior art;

FIG. 8 illustrates a variation of the structure adopted in an embodiment; and

FIG. 9 illustrates a schematic structure of an electronic camera.

25

050388 "071500

5

10

25

described earlier, the output amplifier 4 provided on the semiconductor substrate 20 together with the photoelectric conversion elements 1 and the charge transfer elements 2 and 3 becomes a local heat source to cause occurrence of "dark current shading."

Accordingly, in this embodiment, an amplifier power control circuit 21 that controls the power to the output amplifier 4 constituting a local heat source to minimize the heat generation at the output amplifier 4 by suppressing the bias current at the output amplifier 4 when it is not necessary to operate the output amplifier 4, is added in the image-capturing device to prevent occurrence of dark current shading.

FIG. 1 illustrates an example in which the amplifier power control circuit 21 is provided on the side where a negative source 6 is located and FIG. 2 illustrates an example in which the amplifier power control circuit 21 is provided on the side where a positive source 7 is located.

The amplifier power control circuit 21 is constituted of a parallel circuit comprising a switch element 9 which is opened / closed by a control signal 11 and a resistor 10. The control signal 11, which is provided from the outside of the image-capturing device, changes the bias current at the output amplifier 4 by

opening / closing the switch element 9. The resistor 10
is provided to adjust the bias current when it is not
necessary to operate the output amplifier 4, and a
resistor 5 adjusts the normal bias current flowing when
5 it is necessary to operate the output amplifier 4.

During a "charge transfer," in which electrical
charges are transferred by the charge transfer elements 2
and 3 to the output amplifier 4 where they undergo charge
/ voltage conversion and amplification, the control
10 signal 11 closes the switch element 9 to short the
resistor 10 and supply a normal bias current I set by the
resistor 5 to the output amplifier 4. Such a charge
transfer during which the output amplifier 4 needs to
operate includes transfer of unnecessary charges
15 described below in addition to an image-capturing charge
transfer, in which image-capturing charges at the
photoelectric conversion elements 1 are transferred to
the output amplifier 4 by the charge transfer elements 2
and 3 to be output to the outside through the output
20 amplifier 4. Namely, it includes the transfer of
unnecessary charges in which some of the unnecessary
charges are transferred to the output amplifier 4 by the
charge transfer elements 2 and 3 and are discharged to
the outside via the output amplifier 4 during the
25 processing for discharging unnecessary charges

implemented before a long exposure period and also prior to a charge read following the long exposure period.

When the output amplifier 4 does not need to operate, i.e., during a "charge non-transfer," in which neither
5 the image-capturing charges nor the unnecessary charges are transferred, the control signal 11 opens the switch element 9 to insert the resistor 10 in series to the resistor 5 to reduce the bias current at the output amplifier 4 to its control value I'.

10 It is to be noted that although power is supplied to the photoelectric conversion elements 1 and the charge transfer elements 2 and 3, the illustration of their power circuits is omitted in FIGS. 1 and 2. In addition, the negative source 6 may be a ground line or a 0V line.

15 Now, a method of determining a resistance value R10 at the resistor 10 required to adjust the bias current I' during a charge non-transfer is explained.

For instance, in order to set the bias current I' at the output amplifier 4 during a charge non-transfer to
20 1/10 of the normal bias current I during a charge transfer, the resistance value R10 at the resistor 10 may be set as below.

$$I = E / (R5 + ZA)$$

$$I' = E / (R5 + R10 + ZA)$$

25 $I' / I = 1/10$

thus; $R_{10} = 9 * (R_5 + Z_A)$

In the equations above, Z_A represents the impedance of the power circuit of the output amplifier 4, R_5 represents the resistance value of the resistor 5 and E represents the source voltage applied between the positive source 7 and the negative source 6.

It is to be noted that while the bias current I' at the output amplifier 4 for a charge non-transfer is set to 1/10 of the normal bias current I for a charge transfer in this embodiment, the setting of the bias current I' for the charge non-transfer is not limited to this example, and it may be set at any level as long as it does not allow occurrence of dark current shading attributable to the heat generated at the output amplifier 4 at least when the shutter speed resulting in a long exposure period is set. The bias current I' at the output amplifier 4 for the charge non-transfer may be set to 0, and in such a case, the resistance value R_{10} at the resistor 10 should be set to an infinite value, i.e., the resistor 10 should be eliminated.

In addition, the amplifier power control circuit 21 may be provided on the semiconductor substrate 20. In this case, power from the positive source 7 and the negative source 6 and the control signal 11 are provided to the amplifier power control circuit 21 on the

semiconductor substrate 20.

FIG. 9 presents a schematic structure of an electronic camera provided with the image-capturing device described above. A taking lens 41 forms a subject
5 image on the light receiving surface of an image-capturing device 42. The image-capturing device 42 captures an image of the subject and outputs image data obtained through the image-capturing operation to a controller 43. The controller 43 performs a specific
10 type of image processing on the input image data and stores the processed image data in a memory card 44. At this time, compression processing is performed as necessary. At a monitor 45, the subject image captured by the image-capturing device 42 or a subject image
15 reproduced by using the data stored in the memory card 44 is displayed. A main switch 46 is operated to turn on/off the power to the electronic camera and a shutter release button 47 is operated to issue an instruction for an image-capturing start. The controller 43 is also
20 connected with a photometering circuit 48, an AF circuit 49 and a battery 50.

FIG. 3 is a time chart of the image-capturing operation performed when the exposure period extends longer than a specific length of time (the shutter speed
25 is lower than a specific value).

00500988 "021500

When the main switch 46 of the electronic camera is turned on at a time point t1, the camera starts an operation. At a time point t2, a halfway down switch (not shown), which is turned on when the shutter release
5 button 47 is pressed halfway down enters an on state, photometering is performed by the photometering circuit 48 and based upon the photometering results, the shutter speed, i.e., the exposure period, is set. At the same time, the focal adjustment state of the taking lens 41 is
10 detected by the AF circuit 49, and based upon the focal point detection results, focal adjustment is performed at the taking lens.

When the shutter release button 47 is pressed all the way down and the release switch (not shown) is turned
15 on at a time point t3, power from the positive source 7 and the negative source 6 is supplied to the output amplifier 4 in the image-capturing device and also power (not shown) is supplied to the photoelectric conversion elements 1 and the charge transfer elements 2 and 3.
20 Concurrently with the power supply, the switch element 9 is closed by the control signal 11 and the normal bias current I for a charge transfer is supplied to the output amplifier 4. The power supplied by the positive source 7 and the negative source 6 and the power supplied to the
25 photoelectric conversion elements 1 and the charge

00500988-021500

transfer elements 2 and 3 is generated by the battery 50.

During the period of time elapsing between a time point t_4 and a time point t_5 after the shutter release until the shutter actually starts to open, a transfer operation by the charge transfer elements 2 and 3 is implemented to discharge unnecessary charges at the photoelectric conversion elements 1 and at the charge transfer elements 2 and 3 to an overflow drain (not shown) provided parallel to the charge transfer element 3.

At this time, since the unnecessary charges cannot be completely discharged to the overflow drain, the normal bias current I is supplied to the output amplifier 4 by closing the switch element 9 with the control signal 11 to enable the output amplifier 4 to operate so that the unnecessary charges that cannot be discharged to the overflow drain are discharged to the outside of the image-capturing device through the output terminal 8 via the output amplifier 4.

At a time point t_7 after the unnecessary charges are discharged, the control signal 11 opens the switch element 9, and the bias current at the output amplifier 4 is reduced to the control value I' to prevent heat generation from occurring at the output amplifier 4 during exposure. It is to be noted that the bias current at the output amplifier 4 may be promptly reduced at the

time point t5, at which the processing for discharging unnecessary charges is completed.

At a time point t6, at which a specific length of time has elapsed after the completion of the processing for discharging unnecessary charges at the time point t5, the shutter starts to open. While the shutter enters a full open state at a time point t8, the exposure starts when a certain length of time has elapsed following the time point t8, and accordingly, the bias current at the output amplifier 4 is reduced at a time point preceding at least the time point t8 at which the shutter becomes fully open. During the exposure, the photoelectric conversion elements 1 store charges corresponding to the light intensity levels.

When the exposure is completed, the shutter is closed at a time point t9, and instead of starting the read of stored charges at time point t10 immediately after the shutter becomes completely closed, unnecessary charges stored at the charge transfer elements 2 and 3 are discharged again after the long exposure period. This processing for discharging the unnecessary charges is implemented prior to the processing for reading out the stored charges since, as the exposure period extends longer, the quantity of unnecessary charges stored at the charge transfer elements 2 and 3 increases to a degree

00500988-021500

that cannot be disregarded. The unnecessary charges stored during the exposure are disposed of by discharging unnecessary charges to the overflow drain as in the pre-exposure discharge processing and also by discharging
5 some of the unnecessary charges that cannot be discharged to the overflow drain to the outside of the image-capturing device through the output terminal 8 via the output amplifier 4. In other words, the control signal 11 closes the switch element 9 at the time point t10 to
10 enable operation of the output amplifier 4 by supplying the normal bias current I, and at a time point t11, the discharge processing is started by engaging the charge transfer elements 2 and 3 in a transfer operation.

When the processing for discharging unnecessary
15 charges ends at a time point t12, the charge transfer elements 2 and 3 start an operation to transfer the stored charges, i.e., the image-capturing charges, at a time point t13. It is to be noted that since the processing for reading out the stored charges starts
20 immediately after the unnecessary charges accumulated during the exposure are discharged, the switch element 9 is sustained in a closed state by the control signal 11, and the normal bias current I for a charge transfer is continuously supplied to the output amplifier 4. In the
25 stored charge read processing, the charges stored at the

photoelectric conversion elements 1 during the exposure are guided to the output amplifier 4 via the charge transfer elements 2 and 3, and the transferred charges are sequentially converted to voltages and then amplified at the output amplifier 4, to be ultimately output to an external image processing circuit (not shown) through the output terminal 8.

When the stored charge read processing ends at a time point t14, the power supply to the output amplifier 4 from the positive source 7 and the negative source 6 and the power supply to the photoelectric conversion elements 1 and the charge transfer elements 2 and 3, are stopped and, at the same time, the switch element 9 is opened by the control signal 11.

As described above, in the image-capturing device having the plurality of two-dimensionally arrayed photoelectric conversion elements 1, the charge transfer elements 2 and 3 that transfer electrical charges from the individual photoelectric conversion elements 1 and the output amplifier 4 connected at the ends of the charge transfer elements 2 and 3 along the direction of charge transfer to convert the charges to voltages and then amplify them, all provided on a single semiconductor substrate 20, the amplifier power control circuit 21 provided to control the power to the output amplifier 4

00500988-021500

in conformance to the control signal 11 provided from the outside varies the bias current at the output amplifier 4 in response to the control signal 11. Thus, it is possible to supply power, i.e., the normal bias current I, to the output amplifier 4 only when necessary. In more specific terms, the normal bias current I is supplied to the output amplifier 4 only when unnecessary charges are discharged and image-capturing charges are read, i.e., only when it is necessary to engage the charge transfer elements 2 and 3 to transfer electrical charges to the output amplifier 4 and to achieve charge / voltage conversion and amplification at the output amplifier 4, and the bias current is reduced to the control value I' lower than the normal bias current I at other times. Consequently, the local heat generation occurring at the output amplifier 4 can be minimized so that an increase in the dark current caused by the photoelectric conversion elements 1 that have become heated in the vicinity of the output amplifier 4 is prevented, to ultimately avoid occurrence of dark current shading in the captured image. In addition, since the bias current at the output amplifier 4 is reduced when it is not necessary to operate the output amplifier 4, battery consumption at the electronic camera is minimized.

FIG. 4 is a time chart of the image-capturing

operation performed when the exposure period does not exceed the specific length of time (the shutter speed is fast, achieving a value smaller than the specific value) in an electronic camera provided with the image-capturing
5 device described above.

When the exposure period is short, achieving a value equal to or less than the specific value, the accumulated quantity of unnecessary charges is small and, as a result, it is not necessary to implement the discharge processing,
10 which, in turn, results in a reduction in the length of time to elapse during the series of image-capturing steps, from the shutter release to the end of the charge read. Thus, during the period of time over which the series of image-capturing steps are implemented, the quantity of
15 heat generated at the output amplifier 4 is small, even if the normal bias current I is continuously supplied to the output amplifier 4 and, therefore, the photoelectric conversion elements 1 in the vicinity of the output amplifier 4 do not become heated to increase the dark
20 current.

The main switch 46 is turned on and the subsequent operation up to the shutter release is performed as in the case in which the exposure period is long, illustrated in FIG. 3. When the shutter release button
25 47 is pressed all the way down and the release switch

09500938-021500

(not shown) is turned on at a time point t_{22} , power from the positive source 7 and the negative source 6 is supplied to the output amplifier 4 in the image-capturing device 42 and also power (not shown) is supplied to the photoelectric conversion elements 1 and the charge transfer elements 2 and 3. Concurrently with the power supply, the switch element 9 is closed by the control signal 11 to supply the normal bias current I for charge transfer to the output amplifier 4.

Subsequently, the shutter is opened and exposure is performed, i.e., storage of charges corresponding to the intensity of light is performed by the photoelectric conversion elements 1 at time point $t_{23} \sim t_{24}$. When the exposure ends and the shutter is closed, the processing for reading out the stored charges immediately starts at the time point t_{24} . The charge transfer elements 2 and 3 perform a charge transfer operation, the charges stored at the photoelectric conversion elements 1 during the exposure are guided to the output amplifier 4 via the charge transfer elements 2 and 3, and the transferred charges are sequentially converted to voltages, are then amplified at the output amplifier 4 and are output to an external controller (image processing circuit) 43 through the output terminal 8.

When the stored charge read processing ends at a

time point t25, the power supply to the output amplifier
4 from the positive source 7 and the negative source 6 and
the power supply to the photoelectric conversion elements
1 and the charge transfer elements 2 and 3, are stopped
5 and, at the same time, the switch element 9 is opened by
the control signal 11.

As described above, the normal bias current I is
supplied to the output amplifier 4 by the control signal
11 when the exposure is performed over a period of time
10 shorter than the specific length of time. When the
exposure period is short, even if the normal bias current
I is supplied to the output amplifier 4, the quantity of
heat generated the output amplifier 4 is small since the
bias current I is supplied only briefly and, therefore,
15 the photoelectric conversion elements 1 in the vicinity
of the output amplifier 4 are not heated to cause dark
current shading. Rather, by supplying the normal bias
current I to the output amplifier 4 during the exposure,
the output amplifier 4 achieves a stable state even
20 during the exposure so that the image-capturing charges
can be read out in a stable manner immediately after the
exposure to achieve a reduction in the overall length of
the image-capturing operation.

An optimal value for each individual image-capturing
25 device should be selected by taking into consideration

the quantities of the photoelectric conversion elements 1
and the charge transfer elements 2 and 3, their
arrangement and the position of the output amplifier 4,
when setting the specific length of time with respect to
5 the exposure period used in the decisionmaking as to
whether or not the bias current to the output amplifier 4
is to be reduced.

It is to be noted that while the explanation is
given in reference to the embodiment on an example in
10 which the present invention is adopted in an image-
capturing device that reads charges at the photoelectric
conversion elements by employing charge transfer elements,
the present invention may be also adopted in an image-
capturing device that reads charges at the photoelectric
15 conversion elements through XY address scanning, a
typical example of which is a MOS image-capturing device,
as long as the charges read out are converted to voltages
and are then amplified by the output amplifier provided
on the same substrate. In such a case, too, advantages
20 similar to those achieved in the embodiment are realized.

In addition, while a single output amplifier 4 is
employed in the embodiment described above, the present
invention may be also adopted in an image-capturing
device in which a plurality of photoelectric conversion
25 elements are divided into a plurality of groups, stored

00500988-021500

charges are read by employing charge transfer elements in the individual groups and charge / voltage conversion and amplification are implemented by providing an output amplifier in correspondence to each group at the end of
5 the charge transfer elements along the direction of transfer. In such a case, too, advantages similar to those achieved in the embodiment are realized.

While an explanation is given above in reference to the embodiment on an example in which the heat source
10 that increases the dark current at the photoelectric conversion elements is an output amplifier, the heat source is not limited to an output amplifier. For instance, in a CMOS image-capturing device, an amplifier 31, an A/D converter 32 or a signal processor 33 such as
15 a DSP may be mounted on the semiconductor substrate 20 together with the photoelectric conversion elements 1, as illustrated in FIG. 8. As explained earlier, the amplifier 31 constitutes a local heat source, and in addition to the amplifier 31, the A/D converter 32 and
20 the signal processor 33, too, generate greater quantities of heat as they are driven at higher speeds to become local heat sources. Thus, the present invention can be adopted in an image-capturing device having local heat sources such as the A/D converter 32 and the signal
25 processor 33 mounted on the semiconductor substrate 20

09500988-021500

together with the photoelectric conversion elements 1.
In such a case, by controlling power supply to the A/D
converter 32 and the signal processor 33 to supply power
only when their operation is necessitated and reducing
5 the bias current when they need not operate, heat
generation can be suppressed, to achieve advantages
similar to those realized in the embodiment explained
earlier.

While an electronic camera provided with the image-
10 capturing device according to the present invention is
explained in reference to the embodiment, the image-
capturing device according to the present invention may
be mounted at an apparatus other than an electronic
camera. In other words, the present invention may be
15 adopted in all types of apparatuses that are provided
with an image-capturing device, including scanners and
facsimile machines. It is to be noted that the
electronic camera may be an electronic still camera that
photographs still images or a video camera that records
20 dynamic images.

While the photoelectric conversion elements 1 are
arrayed two-dimensionally in the embodiment explained
above, the present invention is not limited to these
details. For instance, the present invention may be
25 adopted in a structure achieved by arraying photoelectric

conversion elements one-dimensionally. In other words,
the present invention may be adopted in all types of
image-capturing devices each having an array of a
plurality of photoelectric conversion elements 1 in which
5 the photoelectric conversion elements 1 are thermally
affected by another circuit provided on the same
semiconductor substrate.